

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Applicant of :
 Khandros et al. : Group Art Unit: PTO
 Continuation Application of :
 Prior Application : Examiner:
 Serial No. 09/488,268 :
 Filed: Herewith : Date: September 7, 2000 12/1/00
 For: SEMICONDUCTOR CHIP ASSEMBLIES, :
 METHODS OF MAKING SAME AND :
 COMPONENTS FOR SAME :
 X

1C836 U.S. PTO
 09/656690
 09/07/00

12/1/00
 M. Lerner

Assistant Commissioner for Patents
 Box Patent Application
 Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Respectfully submitted,

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EXPRESS MAIL LABEL NUMBER: EL 408439995US